

Patent Application of HIGUCHI
Serial No.: 09/024,771
Art Unit: 2753

linear record erasing head for erasing signals recorded in the linear track of the magnetic tape, comprising:

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a series circuit connecting in series through a series junction point [between] at least between said linear record erasing head and one of said entire-width erasing head [and said linear record erasing head] and [a] an inductance element [.] —;

[a] an oscillating capacitor connected in parallel with said series circuit;

a transistor having a collector and a base and having an emitter connected to said series junction point;

a direct current [preventive] blocking capacitor connected between one end of said series circuit and said base; and

a bias resistor for [given an] providing a bias voltage to said base.

✓
Claim 2, line 2, change "remainder other than" to — remaining one of — .

Claim 4, line 2, change "head", first instance, to — said — .

Please add new claims 5 and 6.

5. A biasing / erasing oscillator in a magnetic tape recording apparatus having an entire width erasing head for erasing signals recorded in an azimuth track, or the azimuth track and a linear track, of a magnetic tape, and a linear record erasing head for erasing signals recorded in the linear track of the magnetic tape, comprising:

a series circuit connecting in series through a series junction point said entire-width erasing head and said linear record erasing head;

an oscillating capacitor connected in parallel with said series circuit;

a transistor having a collector and a base and having an emitter connected to said series junction point;

a direct current blocking capacitor connected between one end of said series circuit and said base; and

biasing means providing a bias voltage to said base.

6. A biasing / erasing oscillator in a magnetic tape recording apparatus having an entire width erasing head for erasing signals recorded in an azimuth track,

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or the azimuth track and a linear track, of a magnetic tape, and a linear record erasing head for erasing signals recorded in the linear track of the magnetic tape, comprising:

an inductance element;

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a series circuit connecting in series through a series said entire-width erasing head, said linear record erasing head and said inductance element;

an oscillating capacitor connected in parallel with said series circuit;

a transistor having a collector and a base and having an emitter connected to a junction point connecting said inductance element and said linear record erasing head;

a direct current blocking capacitor connected between one end of said series circuit and said base; and

biasing means for providing a bias voltage to said base.

REMARKS

This Amendment is responsive to the Office Action mailed October 6, 1999.

The Examiner's comments in that Office Action have been carefully considered.